

**SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK  
FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR  
CIRCUIT**

Appl. No. : 10/709,461 Confirmation No. 3460  
Applicant : Chi-Ming Hsiao,  
Guang-Kaai Dehng,  
Ming-Horng Tsai,  
Ling-Wei Ke,  
En-Hsiang Yeh,  
Chi-Kun Chiu  
Filed : May 6, 2004  
TC/A.U. : 2816  
Examiner : LAM, TUAN THIEU  
Docket No. : MTKP0067USA1  
Customer No. : 27765

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Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

5 Subject: Response to the Office action mailed 09/16/2004

**INTRODUCTORY COMMENTS**

10 The rejections of claims 2, 11, and 12 made by Examiner are argued. Claim 1 is amended to overcome rejection made by Examiner. No new matter is introduced by this response to office action. Reconsideration of claims 1, 2, 11, and 12 is respectfully requested.